

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	42	((processor cpu microprocessor) and (emulator emulation) and (synchronization synchronized lock\$step synchronous)).clm.	US-PGPUB	OR	ON	2008/11/05 09:47
L6	120	((processor cpu microprocessor microcontroller) and (emulator emulation simulator simulation) and (breakpoint halt debug)).clm.	US-PGPUB	OR	ON	2008/11/05 09:50
L7	4	(microcontroller and (emulator emulation) and (synchronization synchronized lock\$step synchronous)).clm.	US-PGPUB	OR	ON	2008/11/05 12:09
L8	42	((processor cpu microprocessor) and (emulator emulation) and (synchronization synchronized lock\$step synchronous)).clm.	US-PGPUB	OR	ON	2008/11/05 12:09
L9	15	((processor cpu microprocessor microcontroller) and (emulator emulation simulator simulation) and (breakpoint halt debug) and (lock\$step synchronous synchronized synchronization)).clm.	US-PGPUB	OR	ON	2008/11/05 12:10
L10	105	L6 not L9	US-PGPUB	OR	ON	2008/11/05 12:10
L11	105	L6 not L9	US-PGPUB	OR	ON	2008/11/05 12:10
L12	4	(microcontroller and (emulator emulation) and (synchronization synchronized lock\$step synchronous)).clm.	US-PGPUB	OR	ON	2008/11/05 12:10

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